

FIG. 2

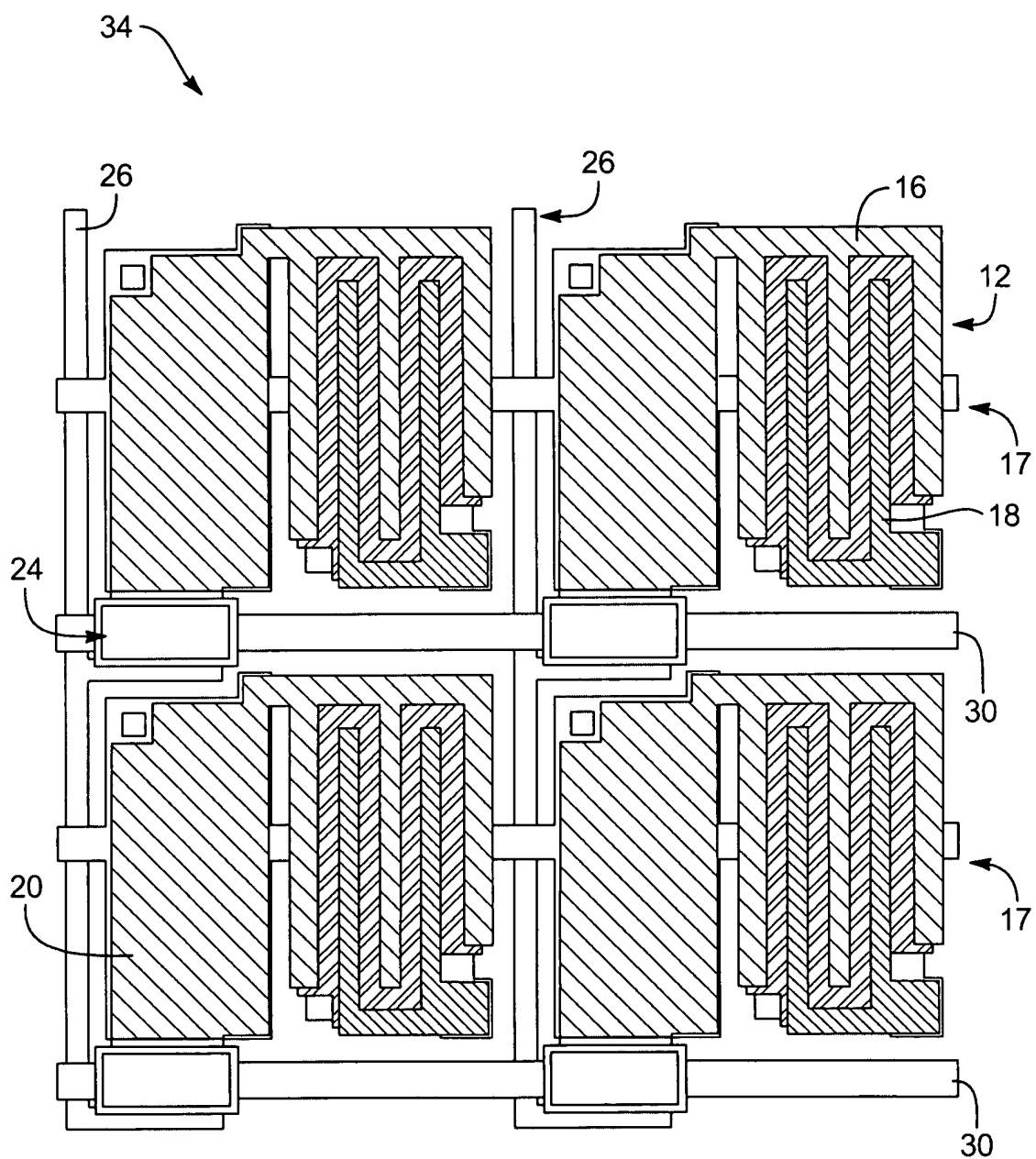


FIG. 3

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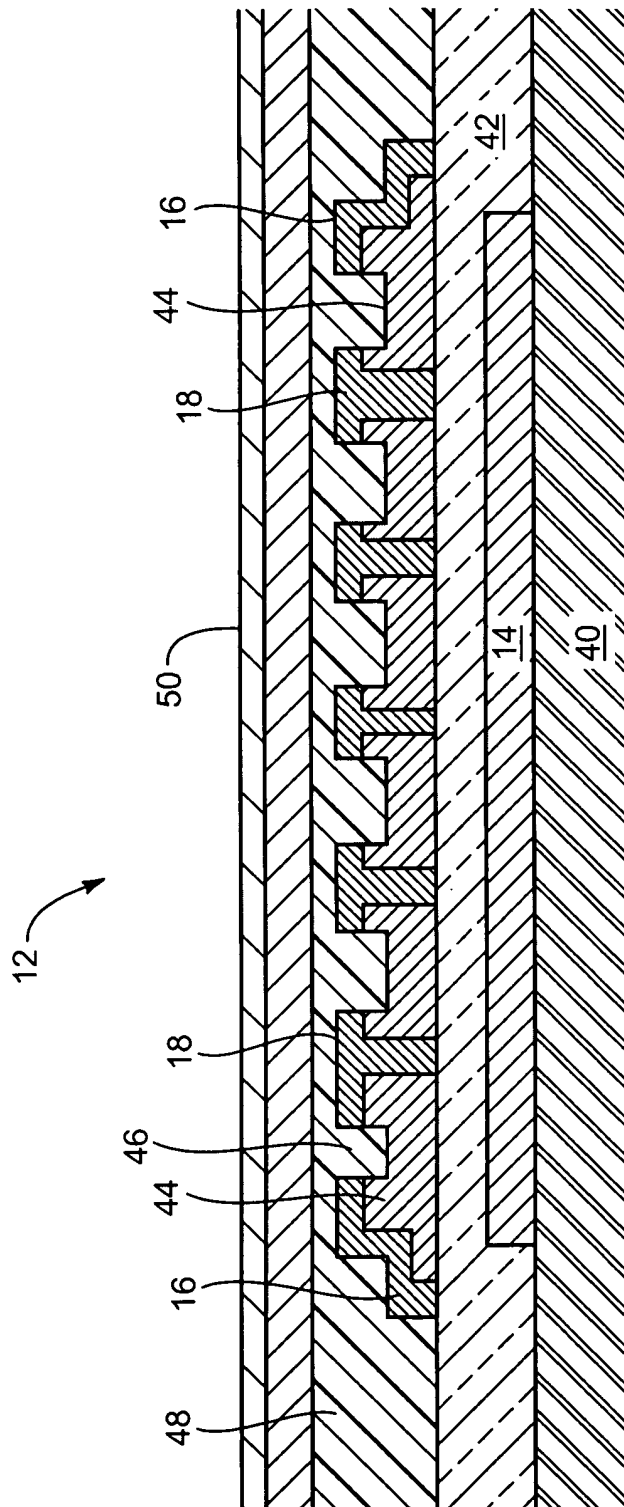


FIG. 4

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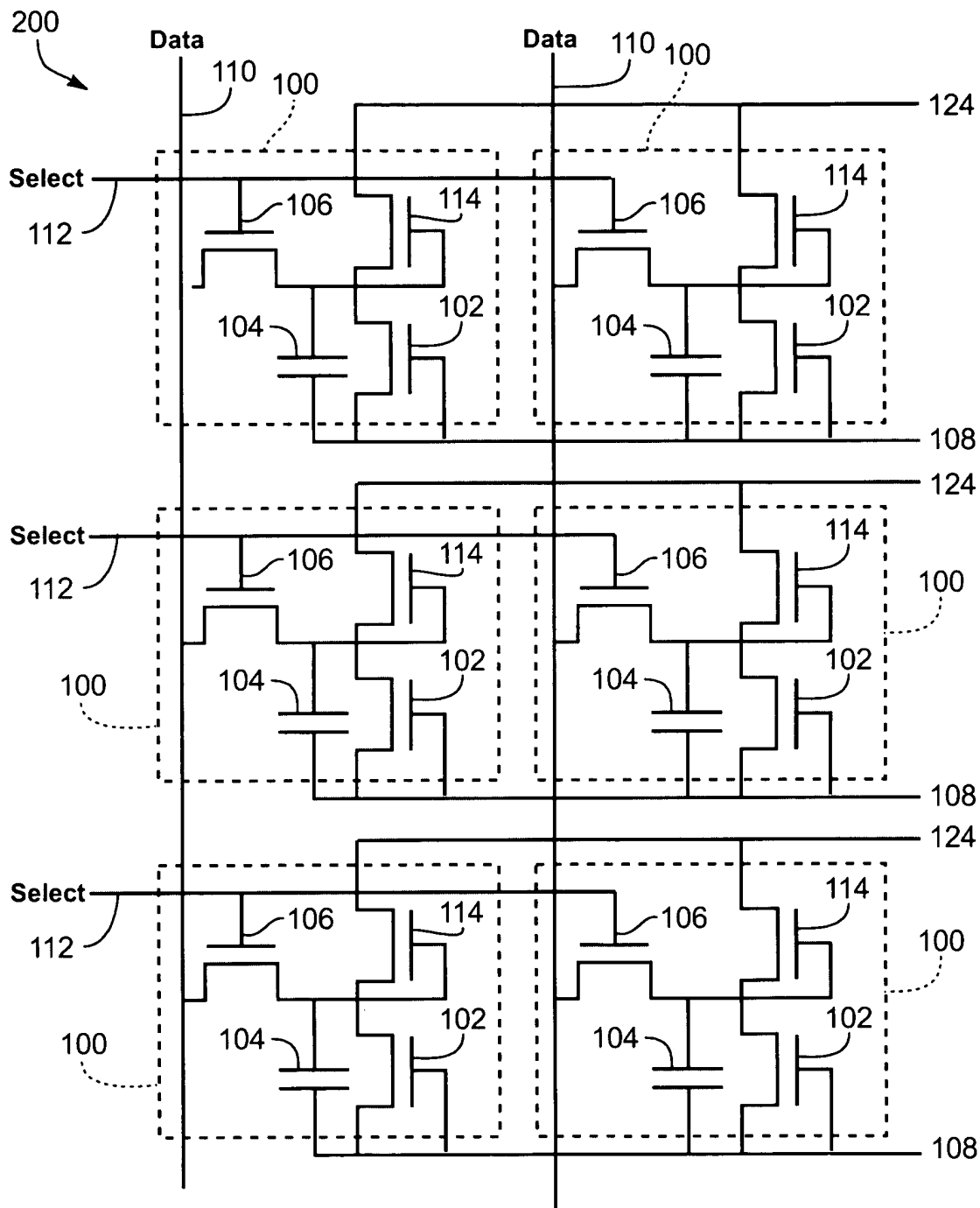


FIG. 8

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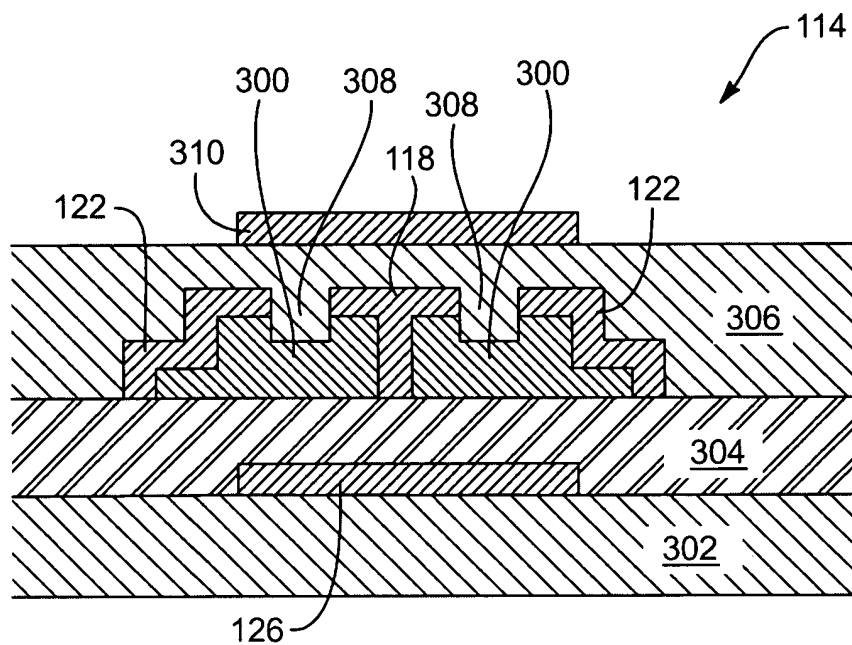
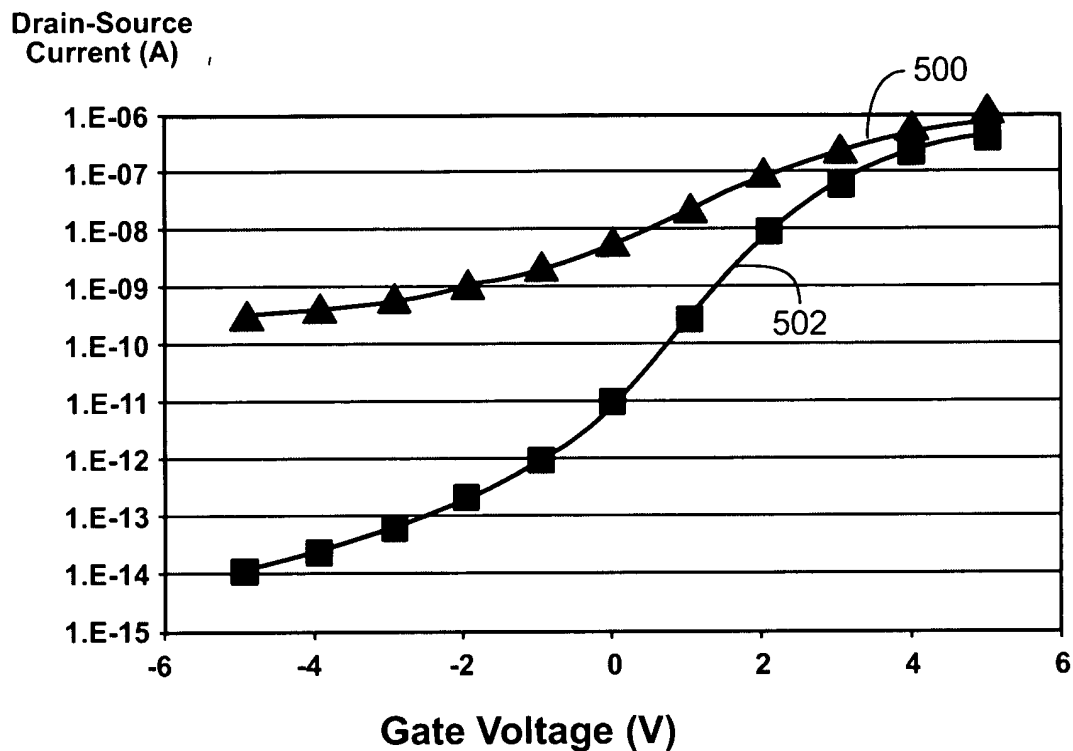


FIG. 10

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Photocurrent and Dark Current vs Gate Voltage (V_{bias1}) at
 $V_{ds} = (V_{bias1}) + 5V$ for a-Si TFT with $W/L = 200 \mu m/6 \mu m$

FIG. 13